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	L11	L10 and gate	40
	L10	L8 and cell	44
	L9	L8 and ramp response	0
	L8	L6 and capacitance	103
	L7	L6 and slope delay	3
	L6	L5 and switching threshold	199
	L5	switch\$ and threshold and slope and delay	8239
	L4	L3 and slope delay	0
	L3	switching threshold delay	8
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☐ 1. Document ID: US 6535987 B1

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L7: Entry 1 of 3

File: USPT

Mar 18, 2003

US-PAT-NO: 6535987

DOCUMENT-IDENTIFIER: US 6535987 B1

TITLE: Amplifier with a fan-out variable in time

DATE-ISSUED: March 18, 2003

INVENTOR-INFORMATION:

NAME

CITY

STATE ZIP CODE

COUNTRY

Ferrant; Richard

Saint Ismier

FR

US-CL-CURRENT: 713/401; 327/170, 716/5

Full Title Citation Front Review Classification Date Reference Security Co. Principles Claims KVMC Draw. De

☐ 2. Document ID: US 6473725 B1

L7: Entry 2 of 3

File: USPT

Oct 29, 2002

US-PAT-NO: 6473725

DOCUMENT-IDENTIFIER: US 6473725 B1

** See image for Certificate of Correction **

TITLE: Method of accurate simulation of logic circuits

DATE-ISSUED: October 29, 2002

INVENTOR-INFORMATION:

NAME

CITY

STATE ZIP CODE COUNTRY

Schoellkopf; Jean-Pierre

Grenoble

FR FR

Hanriat; Stephane

Saint Vincent de Mercuze

US-CL-CURRENT: 703/15; 703/18, 716/2, 716/6

☐ 3. Document ID: US 5378946 A

L7: Entry 3 of 3

File: USPT

Title Citation Front Review Classification Date Reference September Abord medical Claims KWIC Draw De

Jan 3, 1995

Record List Display

US-PAT-NO: 5378946

DOCUMENT-IDENTIFIER: US 5378946 A

** See image for Certificate of Correction **

TITLE: Arrangement for temporal detection of a signal edge of an electrical signal

transmitted over a transmission line

DATE-ISSUED: January 3, 1995

INVENTOR-INFORMATION:

NAME

CITY

STATE

ZIP CODE

COUNTRY

Reime; Gerd Schomberg

DE

US-CL-CURRENT: 327/14; 327/263, 327/361, 327/78

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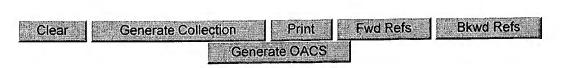
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☐ 1. Document ID: US 6359478 B1

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L3: Entry 1 of 8

File: USPT

Mar 19, 2002

US-PAT-NO: 6359478

DOCUMENT-IDENTIFIER: US 6359478 B1

TITLE: Reduced-undershoot CMOS output buffer with delayed VOL-driver transistor

DATE-ISSUED: March 19, 2002

INVENTOR-INFORMATION:

NAME

CITY

STATE

COUNTRY

Chow; Arnold

Sunnyvale

CA

US-CL-CURRENT: 327/112; 327/391

Full	Title	Citation	Front	Review	Classification	Date	Reference	(B)	Baroll Co.	Claims	KWIC	Drawi D
				440								

☐ 2. Document ID: US 6111443 A

L3: Entry 2 of 8

File: USPT

Aug 29, 2000

US-PAT-NO: 6111443

DOCUMENT-IDENTIFIER: US 6111443 A

TITLE: Accelerated switching by selection of various threshold levels

DATE-ISSUED: August 29, 2000

INVENTOR-INFORMATION:

NAME

CITY

STATE

ZIP CODE

ZIP CODE

COUNTRY

Mores; Robert

Hamburg

DE

Eisele; Harald

Pinneberg

DE

US-CL-CURRENT: 327/206; 327/166, 327/374, 327/74

Full Title Citation Front Review Classification Date Reference Sequences settlements. Claims KMC Draw. De

☐ 3. Document ID: US 5859552 A

L3: Entry 3 of 8

File: USPT

Jan 12, 1999

Mar 17, 1992

Record List Display

• US-PAT-NO: 5859552

DOCUMENT-IDENTIFIER: US 5859552 A

* TITLE: Programmable slew rate control circuit for output buffer

DATE-ISSUED: January 12, 1999

INVENTOR-INFORMATION:

NAME CITY STATE ZIP CODE COUNTRY

Do; Tuan P. San Jose CA Stascausky; Casimiro A. Fremont CA

US-CL-CURRENT: 327/170; 327/276, 327/281



☐ 4. Document ID: US 5189589 A

L3: Entry 4 of 8 File: USPT Feb 23, 1993

US-PAT-NO: 5189589

DOCUMENT-IDENTIFIER: US 5189589 A

TITLE: Power controller

DATE-ISSUED: February 23, 1993

INVENTOR-INFORMATION:

NAME CITY STATE ZIP CODE COUNTRY

Marschall; Klaus W. Meersburg DE

US-CL-CURRENT: 361/93.2; 327/579, 361/111, 361/98

Full	Titl	e Citation	Front	Review	Classification	Date	Reference	260 cm see 1 to ments	Claims	KWMC	Draws De
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	5.	Docume	nt ID:	US 50	97148 A						

File: USPT

US-PAT-NO: 5097148

L3: Entry 5 of 8

DOCUMENT-IDENTIFIER: US 5097148 A

TITLE: Integrated circuit buffer with improved drive capability

DATE-ISSUED: March 17, 1992

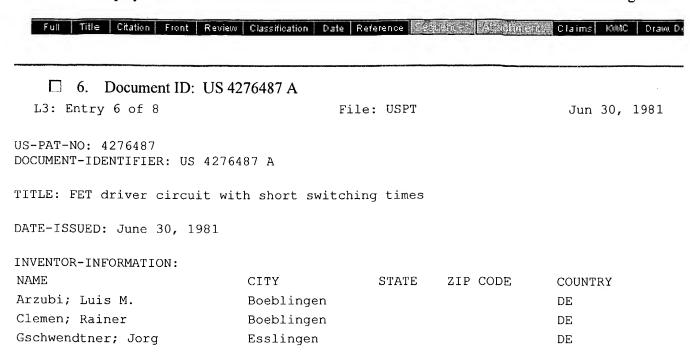
INVENTOR-INFORMATION:

NAME CITY STATE ZIP CODE COUNTRY

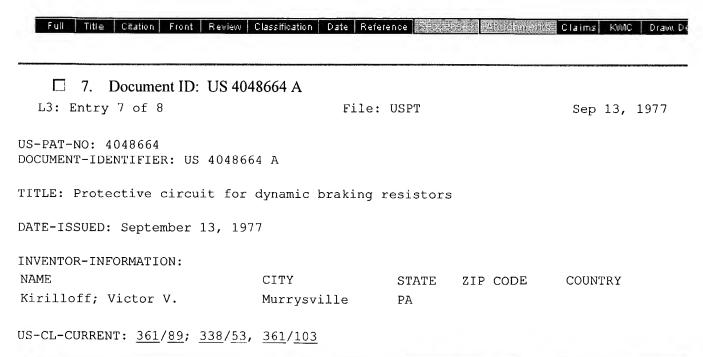
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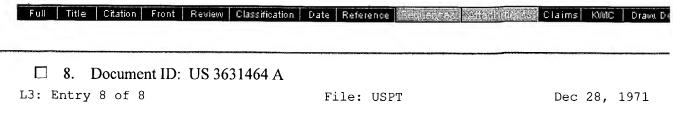
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US-CL-CURRENT: 326/86; 326/27, 326/30, 326/71, 326/81



US-CL-CURRENT: 326/88; 326/17





US-PAT-NO: 3631464

DOCUMENT-IDENTIFIER: US 3631464 A

TITLE: DIGITAL PARALLEL TO SERIAL CONVERTER

DATE-ISSUED: December 28, 1971

INVENTOR-INFORMATION:

NAME

CITY

STATE

ZIP CODE

COUNTRY

Dahlberg; Roger C.

Palo Alto

CA

US-CL-CURRENT: 341/101

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Term	Documents
SWITCHING	458844
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